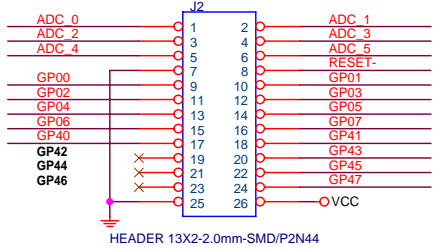
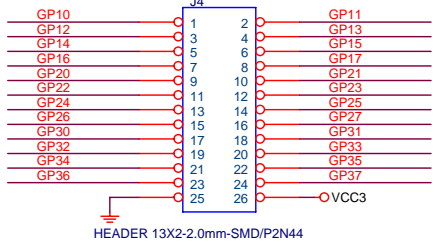


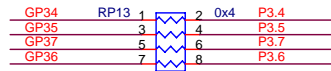
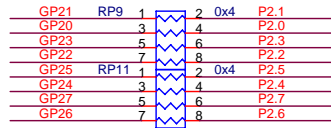
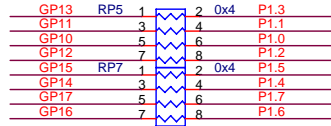
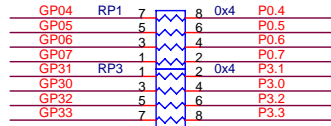
### A/D, P0, P4



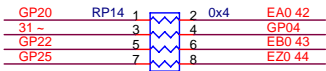
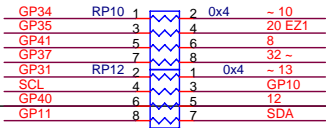
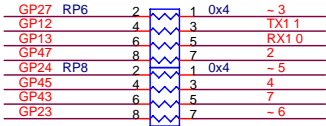
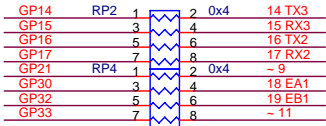
### P1, P2, P3



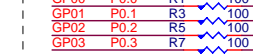
### GPIO MODE OPTION1



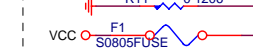
### 86DUINO MODE OPTION2



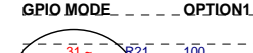
### GPIO MODE OPTION1



### 86DUINO MODE OPTION2



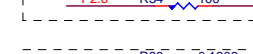
### GPIO MODE OPTION1



### 86DUINO MODE OPTION2



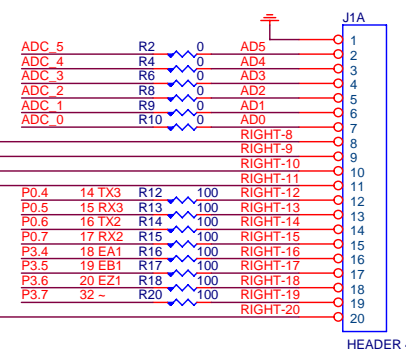
### GPIO MODE OPTION1



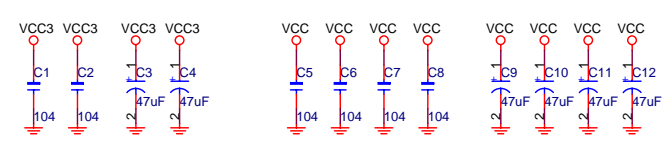
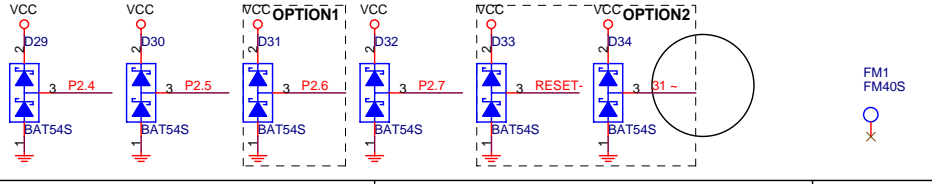
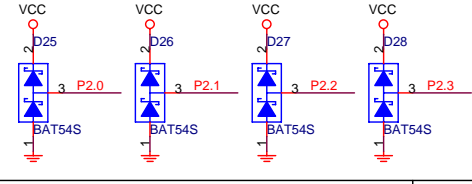
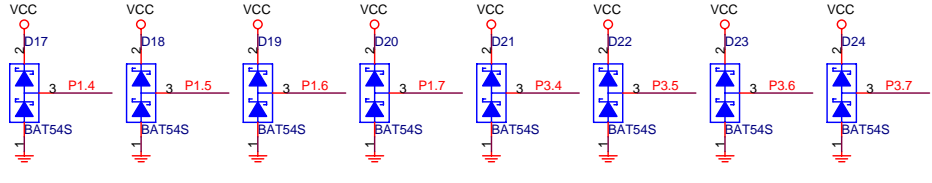
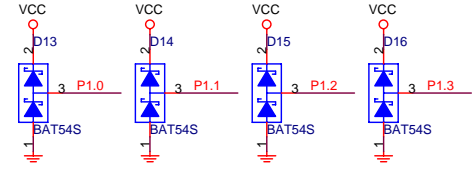
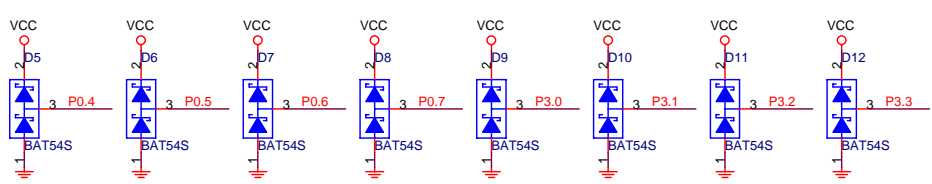
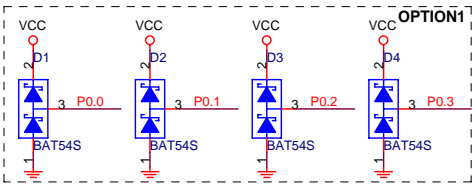
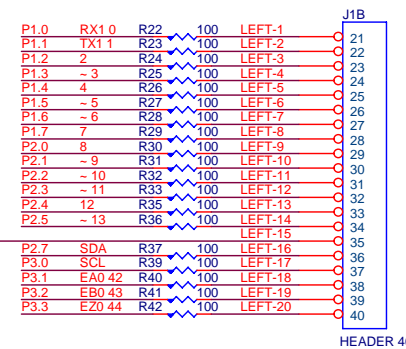
### 86DUINO MODE OPTION2



### BREAD BOARD RIGHT



### BREAD BOARD LEFT



**PCB NO. DM220A**  
**VESA-PC-60 BREAD BOARD DB**  
**86DUINO Educake**  
**V0.3**

**DMP ELECTRONICS INC.**

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